Embedded DSP Processor Design, Vol 2. Systems on Silicon

Description: This book provides design methods for Digital Signal Processors and Application Specific Instruction set Processors, based on the author’s extensive, industrial design experience. Top-down and bottom-up design methodologies are presented, providing valuable guidance for both students and practicing design engineers.

Coverage includes design of internal-external data types, application specific instruction sets, micro architectures, including designs for datapath and control path, as well as memory sub systems. Integration and verification of a DSP-ASIP processor are discussed and reinforced with extensive examples.

FOR INSTRUCTORS: To obtain access to the solutions manual for this title simply register on our textbook website (textbooks.elsevier.com) and request access to the Computer Science or Electronics and Electrical Engineering subject area. Once approved (usually within one business day) you will be able to access all of the instructor-only materials through the "Instructor Manual" link on this book's full web page.

Instruction set design for application specific processors based on fast application profiling
Micro architecture design methodology
Micro architecture design details based on real examples
Extendable architecture design protocols
Design for efficient memory sub systems (minimizing on chip memory and cost)
Real example designs based on extensive, industrial experiences

Contents:
Introduction to DSP and CPU
Finite length DSP
Architecture and Micro architecture design
Instruction set design ? part I
Instruction set design ? part II
ALU and Register file (RF)
MAC (Multiplication and accumulation unit)
Memory sub system and addressing unit
Control path
Design of tools for firmware programmers
Firmware design
Peripheral of DSP cores and processors
Accelerators
Advanced architecture ILP (Instruction level parallelism)
Advanced architecture (On Chip multiple DSP cores)
Design for integration
Review of the design flow and functional verification

Ordering:
Order Online - http://www.researchandmarkets.com/reports/1760218/
Order by Fax - using the form below
Order by Post - print the order form below and send to
Research and Markets,
Guinness Centre,
Taylors Lane,
Dublin 8,
Ireland.
Fax Order Form
To place an order via fax simply print this form, fill in the information below and fax the completed form to 646-607-1907 (from USA) or +353-1-481-1716 (from Rest of World). If you have any questions please visit http://www.researchandmarkets.com/contact/

Order Information
Please verify that the product information is correct.

| Product Name: | Embedded DSP Processor Design, Vol 2. Systems on Silicon |
| Web Address:  | http://www.researchandmarkets.com/reports/1760218/ |
| Office Code:  | SCEJISDX |

Product Format
Please select the product format and quantity you require:

| Quantity | Hard Copy (Hard Back): | USD 85 + USD 28 Shipping/Handling |

* Shipping/Handling is only charged once per order.

Contact Information
Please enter all the information below in BLOCK CAPITALS

<table>
<thead>
<tr>
<th>Title:</th>
<th>Mr ☐ Mrs ☐ Dr ☐ Miss ☐ Ms ☐ Prof ☐</th>
</tr>
</thead>
<tbody>
<tr>
<td>First Name:</td>
<td>Last Name:</td>
</tr>
<tr>
<td>Email Address: *</td>
<td></td>
</tr>
<tr>
<td>Job Title:</td>
<td></td>
</tr>
<tr>
<td>Organisation:</td>
<td></td>
</tr>
<tr>
<td>Address:</td>
<td></td>
</tr>
<tr>
<td>City:</td>
<td></td>
</tr>
<tr>
<td>Postal / Zip Code:</td>
<td></td>
</tr>
<tr>
<td>Country:</td>
<td></td>
</tr>
<tr>
<td>Phone Number:</td>
<td></td>
</tr>
<tr>
<td>Fax Number:</td>
<td></td>
</tr>
</tbody>
</table>

* Please refrain from using free email accounts when ordering (e.g. Yahoo, Hotmail, AOL)
Payment Information

Please indicate the payment method you would like to use by selecting the appropriate box.

☑ Pay by credit card: You will receive an email with a link to a secure webpage to enter your credit card details.

☑ Pay by check: Please post the check, accompanied by this form, to:
Research and Markets,
Guinness Center,
Taylors Lane,
Dublin 8,
Ireland.

☑ Pay by wire transfer: Please transfer funds to:
Account number 833 130 83
Sort code 98-53-30
Swift code ULSBIE2D
IBAN number IE78ULSB98533083313083
Bank Address Ulster Bank,
27-35 Main Street,
Blackrock,
Co. Dublin,
Ireland.

If you have a Marketing Code please enter it below:

Marketing Code:

Please note that by ordering from Research and Markets you are agreeing to our Terms and Conditions at http://www.researchandmarkets.com/info/terms.asp