RISC System/6000 PowerPC System Architecture

Description: Offers support for a wide range of products for the RISC System/6000 product line and AIX operating system, including Uni-processor (UP) and Symmetric Multiple Processor (SMP) systems. Provides important information for building many system features such as memory controllers with caches and bus-to-bus bridges. RISC System/ 6000 PowerPC System Architecture defines an architecture that allows each operating system—in particular, the AIX operating system—to run unchanged on all systems that comply with this architecture. It provides a consistent software interface across a broad range of system implementations and offers all hardware/software dependencies necessary for a successful system identification, configuration and performance tuning process.

An important reference for all programmers and product development engineers who are developing software and hardware products for the RISC/System 6000 PowerPC systems. Also useful for system programmers involved in operating system design, system integrators building products and parts for the system family, and anyone interested in porting other operating systems to the RISC System/6000 family.

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