Phase lock loops (PLL) with frequency synthesisers are key components of modern communication systems. Their growing use in controlling numerous wireless applications continues to drive the need for coherent information on their design and simulation. Balancing a detailed background in theory with practical applications of state-of-the-art device design, this book provides expert advice and in-depth discussions on switching phenomena, higher order loops and noise properties of oscillators, dividers and phase detectors.

Phase Lock Loops and Frequency Synthesis:
- describes the basic properties of PLLs, emphasizing natural frequency and damping factors.
- investigates the theory control system stability.
- examines spurious signals in PLL frequency synthesizers, particularly signa–delta types.
- discusses individual blocks encountered in PLL systems.
- presents practical higher order PLLs and solutions based on the normalized 2nd order forms.

Written by an internationally-recognised expert in the field, this book will prove an essential tool for practising telecommunications engineers developing new systems and components, and professional analog and digital circuit designers. Optical communications engineers, graduate electrical engineering students and researchers will find the practical examples useful.

Contents:

1. Basic Equations of the PLLs.
2. PLLs of the First and Second Order.
3. PLLs of the Third and Higher Orders.
5. Tracking.
6. Working Ranges of PLLs.
7. Acquisition of PLLs.
8. Basic Blocks of PLLs.
10. Digital PLLs (Sampled Systems).
11. PLLs in Frequency Synthesis.
12. PLLs and Digital Frequency Synthesizers.

Appendix: List of Symbols.

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