Algorithms for VLSI Design Automation

Description: Modern microprocessors such as Intel's Pentium chip typically contain millions of transitors. Known generically as Very Large-Scale Integrated (VLSI) systems, the chips have a scale and complexity that has necessitated the development of CAD tools to automate their design. This book focuses on the algorithms which are the building blocks of the design automation software which generates the layout of VLSI circuits. One of the first books on the subject, this guide covers all stages of design.

Contents:

PRELIMINARIES.

Introduction to Design Methodologies.

A Quick Tour of VLSI Design Automation Tools.

Algorithmic Graph Theory and Computational Complexity.

Tractable and Intractable Problems.

General-purpose Methods for Combinatorial Optimization.

SELECTED DESIGN PROBLEMS AND ALGORITHMS.

Layout Compaction.

Placement and Partitioning.

Floorplanning.

Routing.

Simulation.

Logic Synthesis and Verification.

High-level Synthesis.

Appendices.

References.

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