FD-SOI Patent Landscape

Description: This report provides a detailed picture of the patent landscape for Fully Depleted Transistors on Silicon-on-Insulator Substrate (planar FD-SOI and SOI FinFET). It identifies more than 30 patent holders of FD-SOI related intellectual property. It provides in-depth analysis of key technology segments and key players including:

- Time evolution of patent publications and countries of patent filings.
- Current legal status of patents.
- Ranking of main patent applicants.
- Joint developments and IP collaboration network of main patent applicants.
- Segmentation by architecture (planar FD-SOI, SOI FinFET) and process level (transistor, device, circuit).
- Key patents.
- Granted patents near expiration.
- Relative strength of main companies IP portfolio.
- Matrix applicants / technology segments for more than 20 companies.

The “FD-SOI IP” profiles of 10 major companies is presented, with key patents, patented technologies, partnerships, and IP strength and strategy. The report also provides an extensive Excel database with all patents analyzed in the report with technology segmentation. This database allows multi-criteria searches: patent publication number, hyperlinks to the original documents, priority date, title, abstract, claims, patent assignees, technological segments, legal status for each member of the patent family.

Contents:

Scope Of the Report
Rational for Choice
Key Features of the Report
Terminology for Patent Analysis
Methodology
Patent Search Strategy
Technological Segmentation
Architecture: planar FD-SOI AND SOI FinFET
Process level: transistor, device and circuit

Executive Summary

I. FD-SOI Technology Overview

II. Patent Landscape
For each architecture (planar FD-SOI and SOI FinFET):
Time Evolution of Patent Publications
Countries of Patent Filings
Time Evolution by Country of Patent Filing
Main Patent Assignees Ranking
Main Joint Developments and Collaborations
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Countries of Current Main Patent Holders
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III. Focus on Key Players
For all of key player: last news, main collaborations and joint developments, technology choices, portfolio summary, strength/weakness, key patents.

IBM
STMicroelectronics/CEA/Soitec
Samsung
GlobalFoundries/ Advanced Micro Devices (AMD)
TSMC
Intel
Micron Technology
Infineon Technologies
Renesas Electronics
Seiko Instruments
Lapis Semiconductor/Oki Semiconductor

Conclusions

Annexes

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