The thin wafer market is expected to grow at a CAGR of 3.7% between 2016 and 2022 and estimated to reach USD 9.17 billion by 2022. A key driving factor for the growth of the thin wafer market is the reduction in the size of electronic devices. Besides, several other factors such as growing mobile and consumer electronics markets and high amount of material saving are also propelling the growth of the market. “300mm diameter wafer expected to grow at the highest CAGR”:

In the thin wafer market, the 300mm wafer is estimated to grow at a significant pace during the forecast period. Till recent times, semiconductor manufacturers used 150mm and 200mm diameter size wafer for the development of semiconductor devices. Presently, the use of 300mm wafer is increasing due to its high production capacity for semiconductor devices.

“APAC to be the fastest-growing region in the thin wafer market”

The market in APAC is expected to grow at the highest CAGR between 2016 and 2022. The growth is attributed to increasing industrialization and urbanization in this region. The growing semiconductor industry in APAC is the main factor responsible for the increasing adoption of thin wafers in the region. Further, the cheap labor cost, high demand of consumer electronic devices, and favorable economic conditions are some of the reasons for the high growth of thin wafers in this region.

Several primary interviews with experts have been conducted across four major regions, namely, North America, Europe, Asia-Pacific, and Rest of the World (the Middle East, Africa, and South America). The primary participants considered for the study are C level executives, managers, and D level executives of tier 1, tier 2, and tier 3 companies. The breakdown of primary interviews is as follows:

- By Company Type-Tier 1- 55%, Tier 2- 20%, Tier 3- 25%
- By Designation - C Level- 49%, Manager Level- 31%, Director level- 20%
- By Region- North America-20%, Europe-40%, Asia-Pacific-30%, RoW-10%

The key players operating in this market include LG Siltronic, Inc. (South Korea), Shin-Etsu Chemical Co. (Japan), Siltronic AG (Germany), Sumco Corporation (Japan), Sunedision Semiconductor Ltd. (U.S.), SUSS Microtec AG (Germany), Lintec Corporation (Japan), Disco Corporation (Japan), 3M (U.S.), Applied Materials, Inc. (U.S.), Nissan Chemical Corporation (Japan), Synova (Switzerland), EV Group (U.S.), Brewer Science, Inc. (U.S.), and Ulvac GmbH (Germany).

“Reasons to buy the report”:

- This report includes the market statistics pertaining to wafer size, semiconductor device applications, and geography along with their respective revenues.
- The Porter’s five forces framework has been utilized along with the value chain analysis to provide an in-depth insight into the thin wafer market.
- Major drivers, restraints, and opportunities for the thin wafer market have been detailed in this report.
- Illustrative segmentation, analyses, and forecasts for the market based on wafer size, application, and geography have been conducted to give an overall view of the thin wafer market.
- A detailed competitive landscape includes key players, in-depth analysis, and revenue of key players.

Contents:
1 Introduction
1.1 Objectives Of The Study
1.2 Market Definition
1.3 Market Scope
1.3.1 Market Covered
1.3.2 Years Considered For the Study
1.4 Currency and Pricing
1.5 Market Stakeholders
2 Research Methodology
2.1 Research Data
2.1.1 Secondary Data
2.1.1.1 Key Data From Secondary Sources
2.1.2 Primary Data
2.1.2.1 Key Data From Primary Sources
2.1.2.2 Key Industry Insights
2.1.2.3 Breakdown of Primaries
2.2 Market Estimation
2.2.1 Bottom-Up Approach
2.2.2 Top-Down Approach
2.3 Market Breakdown and Data Triangulation
2.4 Research Assumptions and Limitations
2.4.1 Assumptions
2.4.2 Limitations
3 Executive Summary
4 Premium Insights
4.1 Attractive Growth Opportunities in the Thin Wafer Market
4.2 Thin Wafer Market, by Application
4.3 Thin Wafer Market in the APAC Region, 2015
4.4 U.S. to Hold the Largest Share of the Thin Wafer Market in 2015
5 Market Overview
5.1 Introduction
5.2 Market Segmentation
5.3 Market Dynamics
5.3.1 Drivers
5.3.1.1 Reduction in the Size of Electronic Devices
5.3.1.2 Growing Mobile and Consumer Electronics Markets
5.3.1.3 High Amount of Material Saving
5.3.2 Restraints
5.3.2.1 Efficiency Maintenance Is Major Issue in the Thin Wafer Market
5.3.3 Opportunities
5.3.3.1 Growing IC Industry in China
5.3.3.2 High Adoption of Portable Devices
5.3.4 Opportunities
5.3.5 Challenges
5.3.5.1 Volatility and Susceptibility to Damage
6 Industry Trend
6.1 Introduction
6.2 Porter's Five Forces Analysis
6.2.1 Threat of Substitutes
6.2.2 Bargaining Power of Suppliers
6.2.3 Bargaining Power of Buyers
6.2.4 Degree of Competition
6.2.5 Threat of New Entrants
7 Thin Wafer Handling Market, by Process
7.1 Introduction
7.2 Temporary Bonding & Debonding
7.2.1 Market Adhesives
7.2.1.1 UV-Release Adhesives
7.2.1.2 Thermal-Release Adhesives
7.2.1.3 Solvent-Release Adhesives
7.3 Carrier - Less Approach (Taiko Process)
8 Thin Wafer Market, by Wafer Size
8.1 Introduction
8.2 125Mm
8.3 200Mm
8.4 300Mm
9 Thin Wafer Market, by Application
9.1 Introduction
9.2 Mems
9.3 Cmos Image Sensor
9.4 Memory
9.5 Rf Devices
9.6 Led
9.7 Interposers
9.8 Logic
9.9 Others
10 Thin Wafer Market, By Geography
10.1 Introduction
10.2 North America
10.3 Europe
10.4 APAC
11 Competitive Landscape
11.1 Competitive Landscape Overview
11.2 Market Ranking Analysis
11.3 Competitive Situation And Trends
11.3.1 New Product Developments/Launches
11.3.2 Contracts, Agreements, Partnerships, And Collaborations
11.3.3 Mergers & Acquisitions
11.3.4 Expansions And Joint Ventures, (2013-2014)
12 Company Profiles
(Overview, Products And Services, Financials, Strategy & Development)*
12.1 Introduction
12.2 Lg Siltron Inc.
12.3 Shin-Etsu Chemical Co., Ltd.
12.4 Siltronic Ag
12.5 Sumco Corporation
12.6 Sunedison Semiconductor Ltd.
12.7 Süss Micro Tec Ag
12.8 Lintec Corporation
12.9 Disco Corporation
12.10 3M
12.11 Applied Materials, Inc.
12.12 Nissan Chemical Corporation
12.13 Mechatronik Systemtechnik Gmbh.
12.14 Synova
12.15 Ev Group
12.16 Brewer Science Inc.
12.17 Ulvac Gmbh
*Details On Overview, Products And Services, Financials, Strategy & Development Might Not Be Captured In Case Of Unlisted Companies.
13 Appendix
13.1 Insights Of Industry Experts

List of Tables

Table 1 Thin Wafer Market, By Wafer Size
Table 2 Thin Wafer Market, By Application
Table 3 Drivers: Impact Analysis
Table 4 Restraints: Impact Analysis
Table 5 Opportunities: Impact Analysis
Table 6 Challenges: Impact Analysis
Table 7 Thin Wafer Market, By Wafer Size, 2013 - 2022 (Million Square Inches)
Table 8 Thin Wafer Market, By Wafer Size, 2013 - 2022 (USD Million)
Table 9 Thin Wafer Market For 125Mm Wafer Size By Application, 2013 - 2022 (Million Square Inches)
Table 10 Thin Wafer Market For 125Mm Wafer Size, By Application, 2013 - 2022 (USD Million)
Table 11 Thin Wafer Market For 200 Mm Wafer Size, By Application, 2013 - 2022 (Million Square Inches)
Table 12 Thin Wafer Market For 200 Mm Wafer Size, By Application, 2013 - 2022 (USD Million)
Table 13 Thin Wafer Market For 300Mm Wafer Size, By Application, 2013 - 2022 (Million Square Inches)
Table 14 Thin Wafer Market For 300 Mm Wafer Size, By Application, 2013 - 2022 (USD Million)
Table 15 Thin Wafer Market Size, By Application, Million Square Inches 2013 - 2022
Table 16 Thin Wafer Market Size, By Application, 2013 - 2022 (USD Million)
Table 17 Thin Wafer Market For Mems Devices, By Wafer Size, 2013 - 2022 (Million Square Inches)
Table 18 Thin Wafer Market For Mems Devices, By Wafer Size, 2013 - 2022 (USD Million)
Table 19 Thin Wafer Market For Cis Applications, By Wafer Size, 2013 - 2022 (Million Square Inches)
Table 20 Thin Wafer Market For Cis Applications, By Wafer Size, 2013-2020 (USD Million)
Table 21 Thin Wafer Market For Memory Devices, By Wafer Size, 2013 - 2022 (Million Square Inches)
Table 22 Thin Wafer Market For Memory Devices, By Wafer Size, 2013-2022 (USD Million)
Table 23 Thin Wafer Market For Rf Devices, By Wafer Size, 2013 - 2022 (Million Square Inches)
Table 24 Thin Wafer Market For Rf Devices, By Wafer Size, 2013-2022 (USD Million)
Table 25 Thin Wafer Market For Led Devices, By Wafer Size, 2013-2022 (Million Square Inches)
Table 26 Thin Wafer Market For Led Devices, By Wafer Size, 2013-2022 (USD Million)
Table 27 Thin Wafer Market For Interposers, By Wafer Size, 2013-2022 (Million Square Inches)
Table 28 Thin Wafer Market For Interposers, By Wafer Size, 2013-2022 (USD Million)
Table 29 Thin Wafer Market For Logic, By Wafer Size, 2013-2022 (Million Square Inches)
Table 30 Thin Wafer Market For Logic, By Wafer Size, 2013-2022 (USD Million)
Table 31 Thin Wafer Market For Others, By Wafer Size, 2013-2022 (Million Square Inches)
Table 32 Thin Wafer Market For Others, By Wafer Size, 2013-2022 (USD Million)
Table 33 Thin Wafer Market, By Region, 2013-2022 (USD Million)
Table 34 North America: Thin Wafer Market, By Country, 2013-2022 (USD Million)
Table 35 Europe: Thin Wafer Market, By Geography, 2013-2022 (USD Million)
Table 36 APAC: Thin Wafer Market, By Geography, 2013-2022 (USD Million)
Table 37 Ranking Analysis Of Key Players In The Thin Wafer Market, 2014
Table 38 New Product Launches, (2013-2015)
Table 39 Contracts, Agreements, Partnerships And Collaborations, (2013-2015)
Table 40 Mergers & Acquisitions, (2013-2015)
Table 41 Expansions

List of Figures

Figure 1 Thin Wafer Market: Research Design
Figure 2 Market Size Estimation Methodology: Bottom-Up Approach
Figure 3 Market Size Estimation Methodology: Top-Down Approach
Figure 4 Data Triangulation
Figure 5 Limitations Of The Study
Figure 6 Thin Wafer Market, By Wafer Size
Figure 7 Thin Wafer Market, By Application
Figure 8 Global Thin Wafer Market, By Application, 2015
Figure 9 APAC: Thin Wafer Market, By Region
Figure 10 Global Thin Wafer Market, By Region, 2015
Figure 11 Thin Wafers Expected To Witness High Adoption Rate During The Forecast Period
Figure 12 Market For Leds Expected To Grow With Highest CAGR From 2016 To 2022
Figure 13 Taiwan To Hold The Largest Market Share Of The Market In 2015
Figure 14 U.S. And Taiwan Expected To Be The Leading Markets For Thin Wafers In 2015
Figure 15 Decreasing Size Of Electronic Devices Is A Major Driver For The Thin Wafer Market
Figure 16 Reduction In The Thickness Of Mobile Devices
Figure 17 Value Chain Analysis: Thin Wafer Market Major Value Is Added During Fabrication Stage
Figure 18 Porter’S Five Forces Analysis
Figure 19 Porters Five Force Analysis: Impact Analysis
Figure 20 Thin Wafer Market: Threat Of Substitutes
Figure 21 Thin Wafer Market: Bargaining Power Of Suppliers
Figure 22 Thin Wafer Market: Bargaining Power Of Buyers
Figure 23 Thin Wafer Market: Degree Of Competition
Figure 24 Thin Wafer Market: Threat Of New Entrants
Figure 25 Temporary Bonder And Debonder Market Size
Figure 26 Shipments Of 300Mm Wafers Expected To Grow At The Highest CAGR From 2016 To 2022
Figure 27 Thin Wafer Shipments For Led Applications Expected To Grow At The Highest Rate From 2016 To 2022
Figure 28 Memory To Be The Largest Market For Thin Wafers In 2015
Figure 29 300Mm Wafer Size To Hold The Largest Market Share In 2015
Figure 30 300Mm Wafer Market Expected To Grow At The Highest CAGR From 2016 To 2022
Figure 31 Market For 300Mm-Sized Wafers To Be The Largest In 2015
Figure 32 Thin Wafer Market: Geographic Snapshot, 2016-2022
Figure 33 North America: Geographic Snapshot, 2015
Figure 34 Europe: Geographic Snapshot, 2015
Figure 35 APAC: Geographic Snapshot, 2015
Figure 36 Companies Adopted Agreements, Partnerships, And Joint Ventures As The Key Growth Strategy From 2013 To 2015
Figure 37 Thin Wafer Market Growth Approach Analysis, 2013-2015
Figure 38 Geographic Revenue Mix Of Major Companies
Figure 39 Swot Analysis: Lg Siltronic
Figure 40 Shin-Etsu Chemical Co., Ltd.: Company Snapshot
Figure 41 Swot Analysis: Shin-Etsu Chemicals Co.
Figure 42 Siltronic Ag: Company Snapshot
Figure 43 Swot Analysis: Siltronic Ag
Figure 44 Sumco Corporation: Company Snapshot
Figure 45 Swot Analysis: Sumco Corporation
Figure 46 Sunedison Semiconductor Ltd.: Company Snapshot
Figure 47 Swot Analysis: Sunedison Semiconductor Ltd
Figure 48 Süss Micro Tec Ag: Company Snapshot
Figure 49 Lintec Corp.: Company Snapshot
Figure 50 Disco Corporation: Company Snapshot
Figure 51 3M: Company Snapshot
Figure 52 Applied Materials, Inc.: Company Snapshot
Figure 53 Nissan Chemical Corporation: Company Snapshot

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