Texas Instruments DRA726 Jacinto 6 Eco SoC Processor for Automotive Infotainment: Physical Analysis

Description: Infotainment in automotive is growing very quickly as people requiring more and more in-vehicle experiences. It is now one of the most important factors when choosing to buy a new car. Enhanced functions became ordinary in the high-end car segment, but a strong pressure to integrate the same kind of interfaces is now demanded for the entry-level and mid-level vehicles.

Texas Instruments, with the Jacinto 6 Eco processor family address these entry- to mid-level segments by providing a cost-effective solution with feature-rich in-vehicle infotainment. The DRA726 is a System-on-Chip (SoC) able to deliver high-integrity audio, simultaneous multimedia streaming and device connectivity. It includes an ARM Cortex-A15 MPU, a C66x DSP, two ARM Cortex-M4 MPUs and many automotive interfaces to provide a cost-optimized BOM for automotive Tier 1s and car manufacturers.

The SoC is based on enhanced OMAP™ architecture integrated on a 28nm technology and is provided in a 760-ball BGA package featuring TI's Via Channel Array (VCA) technology. It is AEC-Q100 qualified with automotive grade 1 temperature (-40°C to +125°C).

The report provides a complete physical analysis of the DRA726 component with keys information on the packaging and the die (size, process, main blocks...). It also provides an estimation of its manufacturing cost and selling price with forecast for the coming years.

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